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Filing Date	28 February 2002	THEOLI	AFF		
First Named Inventor	Michel Luc Cote	0CT 0 0	2003		
Group Art Unit	1752	0010	2003		
Examiner Name	Unassigned KCS9	SCO TO 4	200		
Attorney Docket Number	NMTI 1002-6		700		

	OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS									
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PTO-1449

Atty. Docket No.

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Serial No.

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Applicant

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Atty. Docket No. Serial No. 10/085,759 NMT1 1002-6 INFORMATION DISCLOSURE **CITATION** Applicant CÔTÉ, Michel Luc PTO-1449 Group Filing Date 1756 2/28/2002 OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.) CITATION **EXAMINER'S** INITIALS Cooke, M., "OPC/PSM Designs For Poly Gate Layers", European Semiconductor, Vol. 22, No. 7, pp. 57-59, July 2000. Granik, Y., et al., "Sub-Resolution Process Windows And Yield Estimation Technique Based On Detailed Full-Chip CD Simulation", SPIE, Vot. 4182, pp. 335-341 (2000). Plat, M., et al., "The Impact of Optical Enhancement Techniques on the Mask Error Enhancement Funchtion (MEEF)", SPIE, Vol. 4000, pp. 206-214, March 1-3, 2000. Mansuripur, M., et al., "Projection Photolithography", Optics & Photonics News 11, 17 pages. February 2000.

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CÔTÉ, Michel Luc

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CÔTÉ, Michel Luc

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Atty. Docket No. Serial No. 10/085,759 NMTI 1002-6 INFORMATION DISCLOSURE **CITATION** Applicant CÔTÉ, Michel Luc PTO-1449 Group Filing Date 2/28/2002 Not Yet Assigned OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.) **EXAMINER'S** CITATION INITIALS Nagahiro, Y., "Improved Mask Technique for Photolithography Applied to 0.25um LSI - Improvement of Resolution, Pattern Correction, Exposure Area", Nikkei Microdevices, pp. 1-6, April 1995. Okamoto, Y., et al., "A New Phase Shifting Mask Technology for Quarter Micron Photolithography", SPIE, Vol. 2512, pp. 311-318 (1995). Pierrat, C., et al., "Required Optical Characteristics of Materials for Phase-Shifting Masks", Applied Optics, Vol. 34, No. 22, pp. 4923-4928, August 1, 1995. Galan, G., et al., "Alternating Phase Shift Generation for Coplex Circuit Designs", SPIE, Vol. 2884, pp. 508-519, September 18-20, 1996. Kanai, H., et al., "Sub-Quarter Micron Lithography with the Dual-Trench Type Alternating PSM", SPIE, Vol. 2793, pp. 165-173 Ishiwata, N., et al., "Novel Alternating Phase Shift Mask with Improved Phase Accuracy", SPIE, Proceedings Of The 17th Annual Symposium On Photomask Technology And Management, Vol. 3236, pp. 243-249 (1997). Morimoto, H., et al., "Next Generation Mask Strategy - Technologies are Ready for Mass Production of 256MDRAM?", SPIE, Vol. 3236, pp. 188-189 (1997). Roman, B., et al., "Implications of Device Processing on Photomask CD Requirements", SPIE, Vol. 3236 (1997) (Abstract Nakae, A., et al., "A Proprosal for Pattern Layout Rule in Application of Alternating Phase Shift Mask", SPIE, Vol. 3096, pp. 362-374 (1997). Tsujimoto, E., et al., "Hierarchical Mask Data Design System (PROPHET) for Aerial Image Simulation, Automatic Phase-Shifter Placement, and Subpeak Overlap Checking", SPIE. Vol. 3096, pp. 163-172 (1997). Yamamoto, K., et al., "Hierarchical Processing of Levenson-Type Phase Shifter Generation", Jpn. J. Appl. Phys., Vol. 36, Part 1, No. 12B. pp. 7499-7503, December 1997. Gordon, R., et al., "Design and Analysis of Manufacturable Alternating Phase-Shifting Masks", Bacus News, Vol. 14, Issue 12, pp. 1-9; December 1998.- Nara, M., et al., "Phase Controllability Improvement for Alternating Phase Shift Mask", Dai Nippon Printing Co. Plud. (16 Ohnuma, H., et al., "Lithography Computer Aided Design Technology for Embedded Memory in Logic", Jpn. J. Appl. Phys. Vol. 37, Part I, No. 12B, pp. 6686-6688, December 1998. **EXAMINER:** Date Considered:

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Serial No. Atty. Docket No. NMTI 1002-6 10/085,759 ÎNFORMATION DISCLOSURE CITATION **Applicant** CÔTÉ, Michel Luc PTO-1449 Group Filing Date 2/28/2002 Not Yet Assigned OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.) **EXAMINER'S** INITIALS Fukuda, H., "Node-Connection/Quantum Phase-Shifting Mask: Path to Below 0.3um Pitch, Proximity Effect Free, Random Interconnects and Memory Patterning", J. Vac. Sci. Technol. B, Vol. 17, No. 6, pp. 3291-3295. November/December 1999. Spence, C., et al., "Integration of Optical Proximity Correction Strategies in Strong Phase Shifters Design for Poly-Gate Layers", Bacus News, Vol. 15, Issue 12, pp. 1, 4-13, December 1999. Kuo, C., et al., "Extension of Deep-Ultraviolet Lithography for Patterning Logic Gates Using Alternating Phase Shifting Masks", J. Vac. Sci. Technol. B, Vol. 17, No. 6, pp. 3296-3300, November/December 1999. Palmer, S., et al., "Dual Mask Model-Based Proximity Correction for High Perforance 0.10um CMOS Process", The 44th International Conference on Electron, Ion and Photon Bearn Technology and Nanofabrication Abstracts, pp. 18-19, May 30-June 1.3.000 Pierrat, C., "Investigation of Proximity Effects in Alternating Aperture Phase Shifting Masks", Numerical Technologies, Inc. (1) pages). Kikuchi, K., et al., "Method of Expanding Process Window for the Double Exposure Technique with alt-PSMs", Optical Microlithography XIII, Proceeding of SPIE, Vol. 4000, pp. 121-131 (2000). Cote, M., et al., "A Practical Application of Full-Feature Alternating Phase-Shifting Technology for a Phase-Aware Standard-Cell Design Flow", Numerical Technologies Inc. (6 pages). Heng, F., et al., "Application of Automated Design Migration to Alternating Phase Sifht Mask Design", IBM Research Report RC 21978 (98769), February 26, 2001 (7 pages). Wong, A., et al., "Alternating Phase-Shifting Mask with Reduced Aberration Sensitivity: Lithography Considerations", Proc. SPIE, Vol. 4346, pp. 1-9 (2001).

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